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MANUAL

# ND-100 TRACER DESCRIPTION

USER GUIDE

Preliminary

Written:

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21.05.1986

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# PREFACE

THE PRODUCT

THE READER

The ND-100 Tracer is intended to be a debugging tool for the ND-100 and the ND-110 bus system. It is based upon two cards:

- ND-100 Tracer 3092.
- ND-100 Trace Adapter 3116.

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PREREQUEST KNOWLEDGE

The manual

Chapter one

Chapter two

Chapter three

Chapter four

Chapter five

The readers of this manual is system programmers and system service personnel. Mainly designers and support people on SINTRAN, XMSG and similar products. Also persons involved in building up ND-100 or ND-110 system can use this tool in troubleshooting activities. Education on ND-100 functionallity and SINTRAN design.

The reader is assumed to have a general knowledge about the ND-100 and the ND-110 functionality and the bus system used for those computers.

This manual is a product description and operation instruction for the ND-100 Tracer.

This chapter gives a functional introduction to the product.

Chapter two contains physcical specifications.

This chapter tells how to install the Tracer into a ND-100 or ND-110 computer.

Chapter four is a functional description of the Tracer based on a block diagram.

This chapter tells about what the Tracer can do and how to use it. It also contains software implemented commands description.



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1 PRODUCT INTRODUCTION

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### ND-100 TRACER Description Product introduction

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# 1.1 ND-100 TRACER INTRODUCTION

### Debugging tool

### MC68000

MONITOR

The ND-100 TRACER is intended to be a software debugging tool, and can be used for the ND-100 and the ND-110 computers. For the ND-100, both the ND-100 Tracer and the ND-100 Trace Adapter cards have to be installed. The ND-100 Trace Adapter card is an interface to the MMS (Memory Management System), and the test plug (B-bus) is used for this connection. When debugging the ND-110 system only the ND-100 Tracer have to be installed. (The MMS on B-bus). The module has a MC68000 microprocessor with local EPROM, RAM and a Trace Memory. The MC68000 is controlled by a MONITOR residing in a PROM. The user communicates with the Tracer through a Tanberg Notis terminal connected to the front of the Tracer printed circuit board. The Trace Module can be placed into the ND-100 card crate, and connection to TELEFIX communication line is implemented.

# 1.2 FEATURES OF THE TRACER.

The ND-100 Tracer can stop the ND-100/110 on any combination of physical address and data on the ND-100 bus. The Break (Trig) condition can be:

- IOX accesses.
- CPU Read or Write accesses.
- DMA Read or Write accesses.

These conditions can be done on all or certain levels. CPU addresses can be Physical or Logical. The ND-100 Tracer has a 4K Trace Memory containing what kind of ND-100 instructions that has been executed. For each memoryreference or IOX cycle on the ND-100 bus [except memory refresh] S0 bits of information about this cycle can be stored into the Trace Memory. The collection of these data are done without interfering the busactivity in any way. It can run DMA, and be used as a performance analyzer. The last feature can be used when measuring time between programmed events.

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MAIN PARTS OF OPERATION:

The MONITOR translates bit numbers and values in the Trace Memory and TRIG/QUALIFICATION masks into symbolic names and numbers that the user can undestand better.

Operation examples

Address bits are operated on as 16 bits logical and 24 bits physical address integers. Data fetched as instructions from the ND-100 CPU ,are dissasemled. The specification of TRIG/QUALIFICATION conditions and displaying the Trace Memory are done by editing screen oriented menus and pictures.

### Exampel of part of the Trace Memory display:

ND-100 TRACER CONTENT

L IRDIAIP REL. Ρ TO PHYSICAL Ι LOGICAL EXTRA FORMAT E O / M / P O O TIME TRIG. BANK. ADDR. T ADDR. DATA DATA V X W A D T N N INTERVAL -0007 001 105050 00 025050 140065 SKP IF DA EQL ST 01 R Ι I P 0000 mic. -0006 001 105052 00 025052 044320 LDA \*-060 01 R I I P 0000 mic. -0005 001 104772 00 024772 000000 9393 <== 01 R D I P 0000 mic. -0004 001 105053 00 025053 135124 JPL I \*+124 01 R Ĩ I P 0001 mic. ==> "a -0003 001 105177 00 025177 021141 01 I P 0001 mic. R D 150405 PIOF -0002 000 021141 00 021141 01 R I I P 0001 mic. -0001 000 021142 00 021142 006000 STA ,X +000 01 R 1 0002 mic. 9393 <== 000000 T0000 000 000001 00 000001 01 W D 0000 mic. +0001 000 021143 00 021143 150412 PION 01 R I 0000 mic. +0002 000 021144 00 021144 146142 COPY SL DP 01 R I I P 0006 mic. +0003 001 105054 00 025054 124002 JMP \*+002 1 R 10 I P 0001 mic. I P 0001 mic. +0004 001 105056 00 025056 124100 JMP \*+100 01 R I MIN \*-152 +0005 001 105156 00 025156 040226 01 I P 0001 mic. R 1 ==> R # +0006 001 105004 00 025004 151243 01 R D 1 P 0001 mic. +0007 001 105004 01 025004 151244 ==> R \$ 01 W D I P 0000 mic.

THE USAGE OF THE TRACER:

• It can be used together with a program listing to follow the sequence of program execution around a TRIG condition.

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- The ND-100 TRACER can be used to "freeze" a ND-100 system when a special event occurs. The tracer can STOP ND-100 when a TRIG occures. The tracer can TRIG when ND-100 STOPS.
- It can be used as a tool when debugging some hardware errors. [CPU errors, DMA devices behaving strange, overwriting of memory, memory out of range errors, IOX errors etc].
- It can measure the approximate time interwall between special events. (Some types of performance measuring, time consumption of special parts of code or debugging asyncronous time critical events.)
- The most obvious area of use for the tracer is as a tool in debugging and documentation of bugs in a ND-100 operating system (SINTRAN, XMSG...).

Memory and register dump can tell quite a lot of the state of a "frozen" system. [ The datastructure in memory can be inspected.]

The Trace Memory content can often tell the story that lead to this situation. [Sequence of intructions, addresses, levels, pagetables...]. Together they can help support people to get an overview of the error and guide them through the listing easier. Also people that don't know the code and datastructures in detail.

The Trace Memory contain 4096 bus cycles and by a reasonable QUALIFICATION they can contain relevant information about the reason for the error. (ex. ignore the idle loop on interrupt level 0 ).

• It can be used by non experts to transport information to experts about system stops or other errors. The tracer can be run in REMOTE mode over a modem and the expert can run the tracer from his office on a machine anywhere. The expert can inform a service person what he shall

trig on (or send him a file with a TRIG/QUALIFICATION setup). Then he can bring the Trace Memory content back on a floppy to the expert.

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•	2 ND -	100	TRACER	SPECIFICATI	ons

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# BAUD RATE SWITCH SETTINGS.

Pos.	Baudrate	Pos.	Baudrate	Pos.	Baudrate	Pos.	Baudrate
0	110	4	1200	8	2400	12	75
1	150	5	1800	9	600	13	50
2	300	6	4800	10	200	14	100
3	2400	7	•• 9600 ••	11	134.5	15	100



The ND-100 Tracer Adapter is used as an interface to the MMS-bus in the ND-100 computer system. Unlike the ND-110, the ND-100 has one extra card for the MMS. Due to this fact, only the test connector (MMS-bus) in position 0 in the Card Crate is used.

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This illustration tells about how to install the debugging tool into a ND-100 computer. The card positions are numbered as in the ND-100 Card Crate, and Pos.x should be the first

The figure is a front view of the cards placed in the Crate. The pos.O is the test

### INSTALLING PROCEDURE

Large cabinet (22 pos): Small cabinet (12-pos) except Compact and Satelite

- 1.Stop the system.
- 2.Protect disks. The protect switch is located at the front of the disk drives.
- 3.Turn the power off. The power switch is located at the front panel.
- 4.Install 3116 in position 0.
- 5.Install Terminal cable. If current loop: Part no. 325307. If RS232 : Part no. 325303.
- Turn the power on.
- Do a reset on 3092B.



ND-100 TRACER Description Installation Description

4 ND-100 TRACER FUNCTIONAL DESCRIPTION

4.1 BLOCK DIAGRAM PRESENTATION

THE MC68000 PROCESSOR	The ND-100 Tracer is using a standard 68000 microprocessor with 8 Mhz clock frequency. The design is similar to the MFbus Controller 3156. The processor has its own clock/interrupt chip (MM58274) with battery backup. Due to this fact it is posible to know the times when the trace function is started and when a trigger (break) has occured.
ND-100 Bus interface	The ND-100 interface consists of latches for address and data lines. Transievers for ND- 100 bus control signals is also imlemented.
MMS INTERFACE	This interface consists of latches for MMS- bus physical address and data lines.
THE TRIGGER AND QUALIFYER	The Trigger and the Qualifuer is similar in Hardware design. These two units can be programmed to recognize any combination on the ND-100 bus or MMS bus. The data witch will be stored in the Trace RAM, is dependent of the combination the user has made in the software commands awailable. Refer to the Commands spesifyed in ch. 5.
DMA CONTROL	This unit isused to control the DMA feature of the Tracer.
VALID DATA/ADDRESS LATCH	This part is used together with the DMA control to store data and address in a DMA operation. A DMA operation means that any physical memory can be read in the ND-100 memory range.

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THE I/O CONTROL

THE INTERNAL CONTROL

This part consists of control signals decodeing. The control word for the trace part of the Tracer is derived from here. Some enable signals is also implemented as seen in the block diagram.

# THE TRACE FUNCTION AND THE TRACE RAM

A control word [16 bits] and a status word [8 bits] are implemented to control the trace part of the controller. The status part will be handled in the software description part in ch. 5. When starting a trace function the control word will enable the ND-100 bus. Different control signals will latch the address and data at the address and data valied times. A signal derived from the MMS command code will latch the information coming from the MMS-bus. This information can be:

- Logical addresses.
- Levels of operation.
- Page tables.
- ION and PON.
- Instruction fetch.
- CPU or DMA select bit.

This trace information will be written into the Trace RAM (4K x 80 bits) in a five word pattern. Eatch word consists of 16 bits (80 bits together). The Trace RAM works as a ring buffer and the address will be incremented if the data coming in is qualifyed. If the Trigger unit recognize a trig word, it will be sent to som PAL (Programmable Array Logic) . This PAL deceides to stop collecting data after a specified time or also stop the ND-100/110 if wanted.

# THE MULTIPLEXER The multiplexer is operated from the internal processor part of the Tracer. To make read and write operations to the Trace RAM, the multiplexer unit is used. It reads or writes one word at a time. Write operations are done for test and initialization purposes.

# THE EPROM/RAM UNIT

The size of the local EPROM and RAM units are 96 Kbytes and 16 Kbytes. The imlementation is similar to the MFbus Controller card 5156.

THE TIMER

The Tracer provide a possibility to measure the time between every qualified event on the ND-100 bus or the MMS bus. This means that it should be possible to measure the time an interrupt is off or the time a certain routine is using. Some cosiderations should however be taken. The timer operates in hole time units and starts with microseconds. After 1000 microseconds it starts counting milliseconds and so on. This means that the accuracy will be bad on low figures. But figures between 100 - 990 should be selected as suitable range.

4.2 THE TRACE RAM WORD CONFIGURATION

The Trace RAM is five words wide (80bits) and 4K deep. It is used to collect information about ND-100 bus cycles.

The 80 parallell bits in the Trace Memory have these functions:

- 16 bits data
- 24 bits physical address
- 6 bits logical page nr.
- 4 bits pagetable nr.
- 4 bits interrupt level.
- 1 bit paging on/off
- 1 bit interrupt on/off
- 1 bit access via normal/alternative page table
- 1 bit Instruction fetch or data access.
- 1 bit CPU or DMA access.
- 1 bit read or write access.
- 1 bit IOX cycle performed.
- 16 bits time information gives intervall between qualified events.

Between the Trace Memory and the ND-100 bus there is some programmable hardware "masklogic". One part of these masks can be used as a filter to select only special types of cycles to be stored in the Trace Memory.

Only DMA cycles or interrupt level 12 activity. This selection is called QUALIFICATION.

The masks can also be used to set up that a special cycle shall cause a TRIG condition in the tracer.

A write O in physical address 44025b. A TRIG condition informs the tracer to stop collecting data into the Trace Memory either immediately or after a selected number of more QUALIFIED cycles.

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Example:

QUALIFICATION

# TRIG SPECIFICATION

Example:

All 80 bits in the Trace Memory Word (exept for time intervall bits) can be used to specify conditions for QUALIFICATION and TRIG.





Timer Status Word 5

	1	1	1	I	I	ł	1	1	1		1	I	I	1	
	_1	1	L	1	1	I	1	1	1_	1	1	١.	1	1	
0														1	5

The memory word no 5 contains the local timer information.

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ND-100 TRACER Description

ND-100 Tracer Functional Description

5 Tracer User guide

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# 5.1 ND-100 TRACER MODES OF OPERATION

Local mode:

Remote mode:

The tracer monitor can run in 2 modes LOCAL or REMOTE.

Local mode is when a terminal is connected direct to the asyncronous interface plug on the board. Then the monitor performs all the interactive user communication , displays menues, traceinfo etc.

In Remote mode a cable from the Tracer interface plug is connected to a ND-100 RS-232 interface instead of to a terminal. That ND machine can be the same system as the tracer is plugged into, or another system. Now the tracer is simulated to the user by a ND-100 application program. This program reserves the line connected to the tracer. All the editing and picture display are done in ND-100 and only packed datablocks containing Trace Memory content, setup information , status, and commands are passed between the tracer and ND-100.

The advantage of REMOTE mode is that tracer setup and Trace Memory content can be saved on diskfiles for later inspection and documentation of special conditions. In LOCAL mode the Trace Memorycontent are destroyed when power is turned off the system.

## 5.2 LOCAL MODE OF OPERATION

# 5.2.1 INTRODUCTION

The monitor is residing in PROM and is running on the MC68000 microprosessor on the tracer. The operator communicates with the monitor through a Tandberg Notis terminal connected to a interface on the board. The monitor are ment to help the operator to specify the qualification and trigconditions he wants and to display the tracedata after a trig. This is done by a set of screen oriented menus and pictures. Most pictures have a command input line at the bottom of the screen . Available commands in the picture will then be displayed by the command HELP. Commands can be abbreviated after the same rules as in SINTRAN.

### NOTE!

This part refers to the monitor working in LOCAL MODE !

### 5a2+2nGOMANDS

Special editing characters in the command line are :

<CTRL> D : delete current line <CTRL> A and (DEL> : delete current character <LEFT> and (RIGHT> : arrows can be used to move the cursor in the field. <CTRL> R : typed at any time in the monitor will have the same effect as pushing the reset switch on the board. <HOME>-arrow/<ESC> : return to main menu.

> Some pictures have no command line . Their funtions are performed by typing the contol caracters explained in the information fields in the picture.

IMPORTANT!

The monitor responds on XON/XOFF characters to start and stop the terminal output. This mean that if a XOF ( $\langle CTRL \rangle$  S) is received from the terminal, the monitor output will

"hang" until a XON (<CTRL> Q) is received.

HOW TO START!

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The main menu should be displayed on the connected terminal after one of the following conditions:

- Power up.
- The reset switch on the board are pushed.
- <CTRL> R are typed on the terminal.

If no response!

Check that a correct terminal cable are connected and that the baudrate setting are the same on the board and in the terminal.

### MAIN MENU

AVAILABLE COMMANDS : HELP SPECIAL-FUNCTIONS TRIG-CONDITIONS-SELECT QUALIFICATION-SELECT START-TRACE DISPLAY-TRACE-CONTENT DUMP-PHYSICAL-MEMORY MAINTENANCE CTRL <R> = RESTART

# 5.2.3 NORMAL OPERATION Before starting the trace function the operator should decide 2 things. • What do I want to fill the Trace Memory with (QUALIFIFCATION). • Which condition(s) shall cause the tracing to terminate (TRIG). OUALIFICATION-SELECTION (Exemples): • Everything is collected(default). • Collect all activity except level 0 (idle loop). • Collect only level 14 code and data. • Collect only pagetable 5 and 7 on level 1. • Collect only DMA cycles. • Collect only IOX cycles. • Start collecting trace information and wait for trig only after 40405B is written into physical address 7 (ARM function)..... TRIG-CONDITION-SELECTION (Exemples): • No trigcondition (default). Tracing stops by manual break only. • Trig if ND-100 stops. • Trig if bit 5 is set in the data read in by IOX 502 . • Trig if anything is written into physical address area 20023B-20175B. • Trig if anything but 12 is written to or read from logical address 23054B on pagetable 7. • Trig if interrupt is turned off more than

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400 milliseconds .

Spesifying Conditions!

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- Trig if -1 is written into one of the entries in the pagetables.
- Trig if a sequence of upto 4 specified events occures .

These are only a few examples. The parameters for specifying qualification and trig conditions can be combined together by some rules to specify a large number of combinations.

In most occations the trig-conditions are specifyed by the TRIG-CONDITION-SELECT command and qualification by the QUALIFICATION-SELECT command. Each will display a picture where you can specify values (or don't care) for data ,address and "control bits". It can be specified upto 4 independant conditions for both trig and qualifications. The selection pictures have two main modes with different fields for cpu addresses :

- PHYSICAL ADDRESS MODE: containing physical address and bank number.
- LOGICAL ADDRESS MODE: containing logical address and pagetable number.

Before the tracing is started you must first answer three questions:

- Shall the tracer STOP the ND-100 cpu when a TRIG occurs?
- Shall the tracer TRIG if ND-100 STOPS?
- Shall the tracer stop collecting data immediately when a trig occurs (end trace), or shall it continue to collect a specified number of cycles?

When the specification part is ready the tracing can be started by the START-TRACE command.

Then the tracer goes into a "collect trace mode". The state of tracing and time are displayed continously. This will continue until a TRIG occures or it is aborted manually.

When some data are collected in the Trace Memory, it can be inspected by the DISPLAY-TRACE-CONTENT command.

Inspecting the data:

This will start by display 17 entries around the trig point or up to where the trace was aborted. Then you can move forwards or backwards in the Trace Memory by using the arrows keys or by a MOVE-TO command. If you are looking for anything special you can use the commands GET-FORWARDS or GET-BACKWARDS to set up a value or mask to search for in the Trace Memory.

If you are running in remote mode (through the TM-MONITOR program in a ND system) the tracbuffercontent can be copied from the tracer board and saved on a file for later inspection.

### ND-100 Memory inspection:

Special Functions:

Physical memory in ND-100 can be inspected by the DUMP-PHYSICAL-MEMORY command (only valid in local mode). This command have the same subcommands as DISPLAY-TRACE-CONTENT for navigating and searching.

### 5.2.4 MONITOR COMMANDS DETAILED DESCRIPTION.

The parameters and functions that are most rarely used or most difficult to use or undestand are put into a separate menu called <u>SPECIAL-FUNCTIONS</u>. Examples are: sequence trig, arm function, pagtable accesses, intervalls... If any of the commands under SPECIAL-FUNCTIONS are to be used they must be specified before the normal TRIG-CONDITION/QUALIFICATION selection.

This command call up a menu of subcommands. It contain parameters for qualification and trigcondition spesification that are put in on request from very limited usergroups. Some of them may reqire special knowledge of ND-100 or the tracer. Some command are put here because the use of them are dependant of or put limitations on other parameters. If you think you can achieve the desired result by editing QUALIFICATION-and TRIG-CONDITION-SELECT pictures YOU CAN JUST FORGET THIS COMMAND. If any of the commands under SPECIAL-FUNCTIONS are to be used they must be specified before the normal TRIG-CONDITION/QUALIFICATION selection.

Some of the subcommands can not be mixed and therefore they are separated in groups A,B,

and C. You can select maximum one subcommand from each group.

### Subcommands

Reset All-Special Functions:

Page-Table-Accesess-Only-Mode (group: a).

Qualify-Data-Different-Than (Group: B).

Example

Qualify-Data-Interval (Group: B.) Function: All previously specified special-function parameters are ignored.

Function: When this mode is set the data and addresses are taken from the B-connector of the ND-100 cpu and not the ND-100 bus. Only references in the page index tables in the memory management system will be checked by hardware for qualification, trig, and collected in Trace Memory. It can be used if you have overwritings in the pagetables to stop the ND-100. The Trace Memory will not contain the rest of the memory references of the program that writes in the pagetables. To find the reason this type of error OPCOM has to be used from the CPU console to dump registers and active level.

Limitation: This function can only be used when connected to a ND-100 cpu (not ND-110 series).

Parameters: 16 bits octal data value.

Function: When this mode is set the DATAfield of the QUALIFICATION-SELECT picture are taken as an inverted value.

If 0 is set in the datafield, only bus cyles that matches the rest of the qualifier word and have a datavalue different than 0 will be collected in the Trace Memory.

Sideeffects: when this parameter is set only 1 qualifier word can be used in QUALIFICATION-SELECT picture (in normal mode upto 4 words).

Parameters: lower limit and upper limit as 16 bits octal data values.

Function: When this mode is set the DATAfield of the QUALIFICATION-SELECT picture are taken as an intervall.

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If 123B is set in low limit and 374E is set in upper limit, only bus cyles that matches the rest of the qualifier word and have a datavalue between 123B and 374B be collected in the Trace Memory.

Limitations: Not all combinations of lower and upper limit can be used. The qualification "mask logic" has no lower and upper limit registers so the specified intervall have to be checked and maybe split up by the monitor to find a valid set of bitmasks.Some intevals are not possible to represent by the 4 available qualifier word and then the monitor will display the message: 'Interval use too many select words! '. Then you have to try to change one of the limits. ( The algorithm for calulating the bitmasks from the intervall is quite complex. Therefor it is difficult to put up a rule for wich intervalls are allowed. It is dependant of both the size and limit values. The user are requested to use the "try and see" strategi. Tip :all intervalls starting at 0 or ending at -1 are allowed.)

Sideeffects: when this parameter is set only 1 qualifier word can be used in QUALIFICATION-SELECT picture (in normal mode upto 4 words).

Qualify-Logical-Addr-Interval Qualify-Physical-Addr-Interval (group: b).

Arm-By-First-Qualifier-Word (Group: B). For both these subcommands parameters, functions, limitations, and sideeffects are the same as for QUALIFY-DATA-INTERVAL only refering to logical address or physical address instead of datafield.

Extra limitation: For LOGICAL-ADDR-INTERVALL the intervall can not cross a page limit

Extra sideeffect: When one of these fields are used it is not possible to switch between logical and physical address mode in the QUALIFICATION-SELECT picture.

Function: When this mode is set, the 1 qualifier word in the QUALIFICATION-SELECT picture are taken as an ARM word. This mean that no check for trig condition and no collection of tracedata are performed before a buscycle matches within the ARM word.

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Example

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Trig-Data-Different-Than (Group: C).

Trig-Data-Interval Trig-Logical-Addr-Interval Trig-Physical-Addr-Interval [Group: C].

Sequence-Trig (group: c).

Interrupt-Off-Longer-Than-Trig
(Group: C).

Give-Level17-Interrupts (Group: C).

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Parameter, function, and sideeffects are the same as TRIG-DATA-DIFFERENT-THAN except refering to TRIG data instead of QUALIFY data.

Parameter, function, limitations, and sideeffects are the same as for the corresponding QUALIFY INTERVALLS only refering to TRIG-CONDITION specifications instead of QUALIFICATION.

Parameters:Give number of events in sequence?[2-4]: Only qualf. events significant(Y/N)?

Function: When this mode is set the tracer will trig only if 2,3 or 4 (specified by the user) immediately following events matches within the specified TRIG words. If 'Only qualf. events significant' parameter is set to Yes then only events matching within the qualification are checked against the TRIG words to increment or reset the sequence counter. The sequence of events do NOT have to match the sequence of specified TRIG words, and the sequence counter will incement each time an event matches the same trigword.

Parameters: 'Max. IOF time = 2\*\*N mic. sec. N= ?(OCT)'

Function: This function can be used to TRIG the tracer when the ND-100 cpu run with interrupt system turned off for longer than a specified period. The time period have to be specified as the power of 2 micro seconds. Ex. values of N : 4=16 mic. sec.,6=64 mic. sec., 20 [OCT] = 65.636 milliseconds...

Parameters: Interrupt interval = 2\*'N mic. sec. N= ? (OCT)'

Function: This function can be used to issue periodic hardware interrupts to ND-100 on level 17B from the tracer. This will only be usefull if you have a direct-task program or driver in ND-100 that should run at a fixed

intervall for performance measurement or any other reason. It also requiers that ND-100 have enabled interrupt from level 17B and that the P register and Paging Control Register on level 17B contain correct values. The specification of the period between interrupts follow the same rules as period in INTERRUPT-OFF-LONGER-THAN-TRIG.

Function: Return to MAIN MENU.

This command is used to specify combinations of address. data, and control signals that causes a trig condition. It is possible to specify from 1 to 4 independant trig conditions (called "select words").

Each select word is split up in 5 fields. In addition there are two modes of operation:

- Physical-Address-Mode: DATA, PHYSICAL ADDRESS, CONTROL BITS, PHYSICAL BANK NR., and PRIORITY LEVELS.
- Logical-Address-Mode: PHYSICAL ADDRESS and PHYSICAL BANK NR. fields are replaced by LOGICAL ADDRESS and PAGE TABLE.

Switching between PHYSICAL and LOGICAL address mode is done by typing CTRL> A.

The fields DATA, PHYSICAL ADDRESS, LOGICAL ADDRESS and PHYSICAL BANK NR. are integer data that can be specified in one of the radix modes OCTAL, HEXADECIMAL, or BINARY. The radix mode can be changed for each field individually by typing:

- <CTRL> O = OCTAL;
- <CTRL> Z = HEXADECIMAL;
- $\langle CTRL \rangle B = BINARY;$

when the cursor is in the field.

In addition to numerical digits it can be typed in "X" as a don't care digit ,which will give match for all values of that digit. You may also see digits displayed as "\$" . It means that some but not all bits in this digit are dont't care bits. This may happen if radix mode are changed on fields with "don't care digits".

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EXIT

Trig-Conditions-Select

The CONTROL BITS field can be seen as a 7 bit binary integer where each bit have a special meaning. The "O" and '1' values have the following meaning for the different bits.

bit	valı	le.	explanation
nr.	0	1	
7		IOX	Bit set when an IOX or IOXT cycle are executed. (NOT when the IOX intruction are fetched from memory). NB! For IOX cycles the information in the address field is hardware device number.(Only 10 least sign. bits valid in logical address.). The data field contain the value transfered between the A register and the device .
6	Read	Write	Bus direction of dataflow. Intruction fetch are always read.
5	DMA	CPU	CPU or DMA device causing the memory access. NB! For DMA accesses do only,data , bus direction, and physical address, contain relevant information.
4	Data	Inst.	Memory access decoded as instruction fetch or data.
3	NPT.	APT.	Data access mapped through NORMAL or ALTERNATIV page table when running in 2 bank mode.
2	IOF	ION	Interrupt system turned OFF or ON.
1	POF	PON	Paging system turned OFF or ON.

Setting a bit to "X" mean "don't" care.

PRIORITY LEVEL and PAGE TABLE fields are represented by one digit for each level or page table. They are edited by setting a "1" or "X" for the values that can cause a trig and a "0" for values that shall be ignored.

The 5 fields for one select word are "ANDed" together. It means that there must be match on all 5 fields at the same time to cause a trig.

ı

If you want to specify more than one trigcondition , the next select word can be edited by typing <CTRL> N . You can move back to the previous word with <CTRL> P and clear current select word by <CTRL> C.

Trig will occur if there are match with

all fields in at least one select word.

NOTE! Remember to fill in all fields . If there are "O" in all values of the interrupt level field the select word can never cause a trig (not even for DMA accesses ,IOX accesses or in interrupt off mode.). There are NO warnings if you specify combinations that can not occur.

If you have a ND-100 CPU (not ND-110 CPU) keep the prefetch mechanism in mind (see note under DISPLAY-TRACE-CONTENT) when you set up trig conditions to avoid false trigging. (The instruction after a SKP may be fetched and cause a trig even if it is not executed.)

Exampel of a TRIG-CONDITION-SELECT picture:

* * * * * * *	SELECT	CONDITION	FOR	TRIG					* * *	* * * * *
				С	cntrol	bits				
Data	Physical	address	0:		Read	DMA	Data	NPT	IOF	POF
16 BITS (BIN)	16 BITS	(OCT)	1:	IOX	Write	CPU	Inst.	APT	ION	PON
0000000000001XX	000207			X	1	х	Х	Х	х	х
00000010000000	>000500			1	x	X	X	X	x	x

Physical bank nr 8 BITS (HEX) 01 XX

Pri	iori	ity	lev	/els	5										
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	x	x	X	x	0	0	0	0	x	0	0	0	0	0
X	X	X	x	X	x	X	X	X	X	X	<u>X</u>	<u>X</u>	x	X	X

COMMANDS :

Address mode: <CTRL> A: switch between LOGICAL and PHYSICAL mode. Radix modes: <CTRL> O = OCTAL, <CTRL> Z = HEXADECIMAL, <CTRL> B = BINARY Select words: <CTRL> N = NEXT, <CTRL> P = PREVIOUS, <CTRL> C = CLEAR current. <CTRL> V = X in all fields, <CTRL> F = X in field, <CTRL> D = 0 in field. Arrows <UP>/<DOWN> between fields, <LEFT>/<RIGHT> within field, <HOME> = EXIT.

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**Oualification-Select** 

This exampel are set up with two select words. They will cause a trig:

- 1. If a dataword with value from 4 to 7 are written in physical address 207B in bank nr. 1 when running on one of the levels 13,12,11,10, or 5.
- 2. If IOX 500 are executed with value 200B in the A register.

This command is used if you want to collect only some of the bus cycles into the Trace Memory. You can collect only level 12 code, only pagetable 5 and 7 accesses, only DMA access and so on. Specification of the QUALIFICATION are done exactly the same way as the TRIG-CONDITIONS.

### NOTE!

Trig and qualification are independant. It means that if your specified trig-conditions does not match within your qualifications the tracer will trig but the memory access that caused the trig will not be stored in the Trace Memory.

Start-Trace:

When trig-condition and qualification pictures are edited ready, tracing of memory accesses can be started. This command will first clear old information in the Trace Memory. Then some questions must be answered:

STOP ND-100 if trig ? (y/n): A 'y' here will cause the tracer to assert the stop signal to the ND-100 CPU when a trig condition occurs. This can be used if you want to look around in the ND-100 memory or display registers that were valid when the trig occured. The execution of the ND-100 CPU can be continued by typing a "!" on the ND-100 console.

TRIG if ND-100 stops? (y/n): This will be an extra trig condition in addition to the trig-condition select-words . A 'y' will cause a trig in the tracer if the ND-100 CPU stops.

Specify location of trig point in Trace
Memory :
Start trace=0. Endtrace = 7777 <OCT> .

The Trace Memory contain 10000B entries. The tracer can continue to collect a number of qualified bus cycles after a trig condition have occured before the tracing stops. You can here specify the trig point location (between 0 and 7777 octal). 0 mean that the trig-point is first, 7777 that it is last in the Trace Memory.

When this question is answered TRACING WILL START. The tracing operation will continue until a TRIG occures or it is aborted by the user typing (ESC). The state of tracing will continously be displayed and can have the following values :

- ==> ARM WORD LOADED! WAITING FOR ARM CONDITION
   (Can be seen if special ARM function is used.)
- ==> WAITING FOR TRIG CONDITION
   (Normal state when collecting data in Trace Memory.)

### \*\*\* TRACE FINISHED \*\*\*

=> TRIG OCCURED! WAITING FOR Trace Memory TO BE FILLED'
(Displayed when trig has occured but not enough
qualified events have occured to fill the rest
of the Trace Memory. Only seen when not
'end trace' selected.)

=> TRIG OCCURED! Trace Memory FILLED
 (Normal state after a trig).

\*\*\* TRACE ABORTED \*\*\*
(Tracing aborted by a user break , not by trig.)

Trace Memory

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Display-Trace-Content

After a trace the trace-buffer can be inspected by this command. Exampel of a DISPLAY-TRACE-CONTENT picture.

\* \* \* \* \* \* \*

DISPLAY TRACE CONTENT

\*\*\*\*\*\*

>

All fields exept for the time interval field contain OCTAL numbers.

Explanation of the information in the display-trace-content picture:

REL. TRIG:

Show address in trace-buffer relative to the trig-point. T0000 is the trig-point. Negative values are stored before and positive values after the trig-point.

PHYSICAL ADDRESS:

is displayed as BANK number +
displacement within bank. [1 bank is 64k
16 bit words.]

LOGICAL ADDRESS:

Is displayed as pagetable number (PIT) + 16 bit logical address. The displacement within page is the same for physical and logical addresses. The logical address is irrelevant for DMA acesses. NB! Pagetable field is not correct when connected to the first version of ND-110 cpu.

### DATA:

Is the 16 bits transported to or from memory in instructon fetch, read and write or DMA cycles.

### NOTE!

For IOX cycles the information in the address field is hardware device number.[Only 10 least sign. bits valid in logical address.]. The data field contain the value transfered between the A register and the device .

> EXTRA FORMAT DATA: contain data disassembled when the instruction fetch bit is set, else ASCJI value of the data.

> The CONTROL BITS are represented by: IOX : I or space Read/Write: R or W DMA / CPU : D or space Data/Inst.: D or I APT./NPT. : A or space ION / IOF : I when ION else space PON / POF : P when PON else spaceor space

### TIME INTERVAL:

Contain approximate information of how long time it has taken since previous qualified memory accesses was stored in the Trace Memory.

When the monitor can not find the trigpoint in the Trace Memory it logically uses the last stored entry as trig-point.

5.2.5 COMMANDS TO LOOK AROUND IN THE TRACE MEMORY

TRACE MEMORY REGISTRATIONS Cache or pagetable accesses

The tracer are only detecting accesses going on the ND-100 bus where it is located. Read or write only in cache or pagetables are not seen. To avoid confusion when looking in the Trace Memory ALWAYS TURN THE ND-100 CACHE OFF BEFORE TRACING IS STARTED.

Prefetch

The normal ND-100 CPU (separate CPU and mms board ) are using prefetch on instructions. It means that while one instruction are decoded the next adress are fetched as an instruction from memory even if it is really executed or not. This may lead to false" instructions in the trace after sequencing instructions as JMP, JPL, SKP, MIN, WAIT and so on. The data operated on by an instruction may often lay some location after the instruction in the Trace Memory. Changes in some CPU condtitions as paging and interrupt system may cause the microprogram to fetch the same instruction two or three times before it is executed (it can not trust that the prefetch was correct). This mean that the user have to watch the sequence of logical addresses and type of instructions carefully, to sort out manually which instructions have been executed and which are false prefetches. This can be quite difficult if some SKP, JMP and MIN are following in the trace . Prefetching should also be kept in mind when setting up trig conditions to avoid false trigging.

When the tracer are located on the local ND-100 bus , memory accesses coming from other channels to a shared memory are not seen by the tracer board and therefore not stored in the Trace Memory. This goes for accesses from ND-500 and DMA interfaces located in other crates.

IOX cycles going to the console or the internal realtime clock are not stored in the Trace Memory.

For DMA accesses only physical address, data, and R/W bit contain relevant information.

NEXT-PICTURE / PREVIOUS-PICTURE (or the <UP> / <DOWN> arrows ) will move the display window to the next/previous 20B entries in the buffer. MOVE-TO will move to an absolute octal value in the buffer.

Will search trough the buffer fora specified combination of addresses, data and control bits.<CTRL> G will continue the search specified in the previous GET command.

The tracer can read the ND-100 memory though a DMA interface that read 1 word pr. tranfer.
This command can display and decode the content of physical memory. It has the same

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Accesses in shared memory

IOX internally on the cpu board

DMA

Get-Backwards-Value Get-Forwards-Value

Dump-Physical-Memory

subcommands for moving around and searching as the DISPLAY-TRACE-CONTENT command. Moving and searching are done within one 64k word memory bank at a time. And there is a command SET-BANK-NUMBER to move to another bank.

IMPORTANT!

Remember that the tracer act as a normal DNA device on the bus and if you try to read uninitialized or nonexistent memory the ND-100 CPU will get a MEMORY PARITY ERROR or MEMORY OUT OF RANGE internal interrupt and SINTRAN will probably go in ERRFATAL (STOP). Therefore this command must be used with great care when SINTRAN is running.

### NOTE!

The last page in bank O are never used by SINTRAN and are not initialized exept if a memory test program have been run before SINTRAN is started.

### Maintenance

This contain some subcommands for verification of the tracer board itself and are not used in normal operation. Commands exist for testing Trace Memory,update internal clock and look at internal program memory.

### 5.3 REMOTE MODE OF OPERATION

### 5.3.1 INTRODUCTION

In REMOTE mode a cable from the tracers interface plug is connected to a ND-100 RS-232 interface instead of to a terminal. The ND machine can be the same as the tracer is plugged into or another system. Now the tracer is simulated to the user by a ND-100 application program called TM-MONITOR. This program reserves the line connected to the tracer. All the editing and picture display are done in ND-100 and only packed datablocks containing Trace Memory content, setup information , status, and commands are passed between the tracer and ND-100. The advantage of REMOTE mode is that tracer setup and Trace Memorycontent can be saved on diskfiles for later inspection or documentation of special conditions. In LOCAL mode the Trace Memorycontent are destroyed when power is turned off the system.

# 5.3.2 COMMANDS IN REMOTE MODE

COMMANDS NOT AWAILABLE IN REMOTE MODE.

The commands DUMP-PHYSICAL-MEMORY and MAINTENANCE does not exist in TM-MONITOR program.

CHANGED COMMANDS IN REMOTE MODE.

START-TRACE will send blocks of tracer setup information to the tracerboard. When trace is running it will poll the line to get the state of the tracer. This mean that it takes a short moment from the trig occures until it is reported by the program.

Display-Trace-Content

This command can use two different strategies when displaying and searching through the Trace Memory.

- If COPY-TRACE-FROM-TRACER or READ-TRACE-FROM-FILE command are used, the program's local Trace Memory is always used by GET and display operations.
- If only OPEN-PATH-TO-TRACER command is executed, blocks of Trace Memory Content are fetched over the line when needed for display. If you display the same picture twice no fetching is necessary the second time. GET-FORWARDS/BACKWARD will ask the traceboard to make the search and report back if a match is found.

The second strategy will save the time it takes to copy the entire buffer if you don't want to save it on file, but you may have some extra halts in the display when data is fetched.

### EXTRA COMMANDS IN REMOTE MODE

**Open-Path-To-Tracer** 

Copy-Trace-From-Tracer

Parameter: Peripheral file name of tracer?

Function: This command reserves the periphereal file where the cable from the tracer should be connected. A control character is sent to the tracer to set it in remote mode . Then a message is sent to the tracer to check for responce and get some information about the state of it. If OK the message 'Contact established!' will be displayed. This command must be performed with no errors before the commands START-TRACE, COPY-TRACE-FROM-TRACER or DISPLAY-TRACE-CONTENT are executed.

Parameter: 'COPY ENTIRE BUFFER (Y/n)' If 'N' then in addition 'FROM OCTAL ADDRESS (-7777B:777B)' and 'TO OCTAL ADDRESS (-7777B:777B)'.

Function: The content of the Trace Memory in the ND-100 tracer board is copied over the RS-232 line to a memory buffer in the TH-MONITOR program in ND-100. To copy the entire buffer takes approximately 5 minutes on a 9600 baud line. When copying is finished the

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Save-Trace-On-File

Read-Trace-From-File

Save-Setup-On-File

Read-Setup-From-File

Print-Trace-On-File

user is asked if he wants to save the tracecontent on file.

Parameter: FILE NAME. Default file type is :TRAC

Function: Save the content of the local Trace Memory on a file. One such file need 24 pages of disk space.

Parameter: FILE NAME. Default file type is :TRAC

Function: Read Trace Memorycontent previously written by SAVE-TRACE-ON-FILE This Trace Memorycontent can now be displayed by DISPLAY-TRACE-CONTENT.

Parameter: FILE NAME. Default file type is :STUP

Function: The parameters edited in the pictures SPECIAL-FUNCTIONS, TRIG-CONDITION-SELECT, and QUALIFICATION-SELECT can be saved on a disk file by this command.

Parameter: FILE NAME. Default file type is :STUP

Function: A readymade tracer setup written to file by the command SAVE-SETUP-ON-FILE can be read back by this command. When issuing a START-TRACE command this TRIG and QUALIFICATION setup is sent directly to the tracer board without have to be edited. If wanted this seetup can be inspected and modified the normal way.

Parameter: FILE NAME. Default file type is :PRNT 'FROM OCTAL ADDRESS (-7777E:7777B)' and 'TO OCTAL ADDRESS (-7777B:7777B)'.

Function: This command produce a file containing a part of the Trace Memory content in a ascii decoded format similar to the DISPLAY-TRACE-CONTENT command. This file can be read by an normal editor or printed on a printer. NB! The printout of an entire tracebufer by this command regire 165 pages of disk space.

# 5.3.3 SWITCHING BETWEEN LOCAL AND REMOTE MODE

Switching the tracer board from local to remote mode are done automatic by the OPEN-PATH-TO-TRACER command in TM-MONITOR program. To switch it back to local mode, when a terminal is connected directly to the board, you can type (CTRL) R on the terminal or push the reset button on the tracer. The trace content is not destroyed by any of these swithing in modes. That means that if you have run in local mode and have got something in the Trace Memory that you want to save, you can connect it in remote mode and use the commands OPEN-PATH-TO-TRACER, COPY-TRACE-FROM-TRACER, and SAVE-TRACE-ON-FILE in the TM-MONITOR program. If you are running the TM-MONITOR program on the same machine as the tracerboard is installed, and the TRIG or a bug makes the system to stop, you can either restart your system or connect the tracer locally to display the trace content.

5.3.4 INSTALLATION AND REQUIREMENTS TO RUN IN REMOTE MODE

Hardware requirements.

1 ND-100 TRACER board.

1 cable ND100-ND100 RS 232.

1 free terminal-line switched switched to RS 232.

The terminal-line should run on the same baudrate as the ND-100 tracer board and with even parity.

Software requirements

The program TM-MONITOR:PROG need 120 pages of disk space. It must be run on a Tandberg NOTIS terminal.

On the logical device number of the terminalline to the tracer the following procedure must be performed from user SYSTEM.

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> @SET-PERIPHERAL-FILE "TRACER" (DEVNO)D @SET-FILE-ACCESS TRACER RW,RW,RWD @SINTRAN-SERVICE @REMOVE-EROM-BACK-TABLE (DEVNO)D I Y Y Y @CHANGE-BUFFER-SIZE (DEVNO)D I 1000B Y Y Y @EXIT

The next lines should be put in LOAD-MODE:MODE file. (They must be performed for each warm start.) @SINTRAN-SERVICE @CHANGE-DATA <DEVNO>D'I Y N N ECH/O . @EXIT

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6 TELEFIX CONNECTION

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# 6.1 WHY USE TELEFIX ?

# 6.2 TELEFIX CONNECTION